

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/717,270	TAKEDA ET AL.	
	Examiner	Art Unit	
	Srilakshmi K. Kumar	2675	

-- **The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to \_\_\_\_\_.
2.  The allowed claim(s) is/are 13-21, renumbered to 1-9.
3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All b)  Some\* c)  None of the:
    1.  Certified copies of the priority documents have been received.
    2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

## **DETAILED ACTION**

### ***Allowable Subject Matter***

1. Claims 13-21 are allowed.
2. The following is an examiner's statement of reasons for allowance:

As to independent claim 13, the prior art of record fails to disclose a liquid crystal display comprising a video correction signal generator for said drain line to superpose at least one predetermined correction signal on the video signal of said output buffer for all pixels included in at least one column, said predetermined correction signal being determined in accordance with a pixel included in said at least one column that is at a farthest position of said drain line from said output buffer, a length of said drain line to said pixel in said farthest position from said output buffer being a maximum length with respect to a distance from said output buffer of all other pixels in said at least one column.

As to independent claim 19, the prior art of record fails to disclose a liquid crystal display comprising, a video correction signal generator for each drain line to superpose at least one predetermined correction signal on the video signal of said output buffer for at least two pixels, a first pixel and a second pixel, included in at least one column, wherein said predetermined correction signal is constant for each of said pixels included in said at least one column, said predetermined correction signal being determined in accordance with said second pixel in said at least one column, and said second pixel being located at a position further away from said output buffer than said first pixel.

With respect to claims 14-18, 20 and 21, these claims are allowable as they depend upon an allowed base claim.

The following are prior art to be made of record and are considered to be pertinent to the applicant's disclosure.

Ikeda (US 5,457,474) discloses an active matrix liquid crystal display comprising a device for producing a compensation signal to compensate a source electrode voltage of the TFT for each divided section of the display area which is based on the vertical and horizontal sync signals as opposed to wherein a predetermined correction signal being determined in accordance with a pixel included in said at least one column that is at a farthest position of said drain line from said output buffer as disclosed by the instant application.

Oda et al (US 6,222,516) disclose a liquid crystal display comprising a correction voltage for correcting distortion of a waveform for driving one of the first and second electrodes, which is applied to the third electrode by correcting a distorted waveform by applying a voltage to a given scan bus line that is capacitively coupled with the common electrode during an unselected period of the scan bus line. Oda et al do not disclose the determination of the correction signal as is disclosed by invention of the instant application as shown above.

Shimizu (US 6,259,425) discloses a matrix type display comprising a video signal voltage compensation circuit for suppressing distortion of a video signal produced on a video signal transmission bus mainly by the wiring resistance and a parasitic capacitance. The compensation circuit also includes a filter whose correction coefficient is varied in

response to elapsed time from the reception of a horizontal synchronizing signal. Shimizu does not disclose the determination of the correction signal as is disclosed by invention of the instant application as shown above.

Okumura et al (US 6,331,844) disclose a matrix liquid crystal display comprising a voltage-current converter for receiving the pixel signal of current variables and converting the pixel signal into a voltage signal. Okumura et al do not disclose the determination of the correction signal as is disclosed by invention of the instant application as shown above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Srilakshmi K. Kumar whose telephone number is 571 272 7769. The examiner can normally be reached on 10:00 am to 6:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571 272 3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Srilakshmi K. Kumar  
Examiner  
Art Unit 2675

SKK  
March 16, 2006



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SUPERVISORY PATENT EXAMINER